



#4  
DBL  
9-5-01

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicants:</b>	Eiko Suzuki	<b>Examiner:</b>	John Donaldson
<b>Serial No.:</b>	09/828,003	<b>Art Unit:</b>	2621
<b>Filed:</b>	April 6, 2001	<b>Docket:</b>	14467
<b>For:</b>	ID RECOGNITION APPARATUS AND ID RECOGNITION SORTER SYSTEM FOR SEMICONDUCTOR WAFER		
		<b>Dated:</b>	August 30, 2001

Assistant Commissioner for Patents  
Washington, D.C. 20231

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**INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 C.F.R. §§1.97 and 1.98, it is requested that the following reference, which is also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Laid-Open Patent Publication No. 2000-55820, published February 25, 2000, together with an English-Language Abstract and Japanese Patent Office translation.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on August 30, 2001.

Dated: August 30, 2001

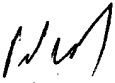
  
Michelle Mustafa

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The reference was cited in a Search Report received from the United Kingdom Patent Office. Applicant is submitting a copy of the above-cited reference, together with a copy of the Search Report. The relevance of the above-identified reference has been described in the Search Report.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

  
Paul J. Esatto, Jr.  
Registration No. 30,749

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PJE:dg

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
14467

In Re Application Of: **Eiko Suzuki**

Serial No.  
09/828,003

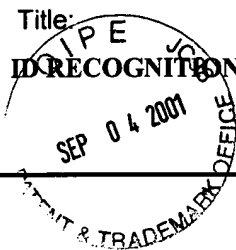
Filing Date  
April 6, 2001

Examiner  
John Donaldson

Group Art Unit  
2621

Title:

**ID RECOGNITION APPARATUS AND ID RECOGNITION SORTER SYSTEM FOR SEMICONDUCTOR WAFER**



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**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☒ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. 19-1013/SSMP as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of \_\_\_\_\_
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**Michelle Mustafa**

\_\_\_\_\_  
Typed or Printed Name of Person Mailing Correspondence

\*This certificate may only be used if paying by deposit account.

Signature

Dated: August 30, 2001

**Paul J. Esatto, Jr.**  
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GP 2621

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(Under 37 CFR 1.97(b) or 1.97(c))

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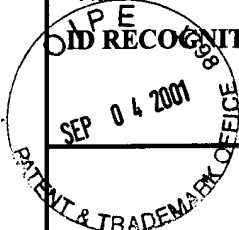
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Address to:  
Assistant Commissioner for Patents  
Washington, D.C. 20231

**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application; within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or before the mailing date of a first Office Action on the merits, whichever event occurs last.

**37 CFR 1.97(c)**

2. ☐ The Information Disclosure Statement submitted herewith is being filed after three months of the filing of a national application, or the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or after the mailing date of a first Office Action on the merits, whichever occurred last but before the mailing date of either:

1. a Final Action under 37 CFR 1.113, or
  2. a Notice of Allowance under 37 CFR 1.311,
- whichever occurs first.

Also submitted herewith is:

- ☐ a certification as specified in 37 CFR 1.97(e);

**OR**

- ☐ the fee set forth in 37 CFR 1.17(p) for submission of an Information Disclosure Statement under 37 CFR 1.97(c).